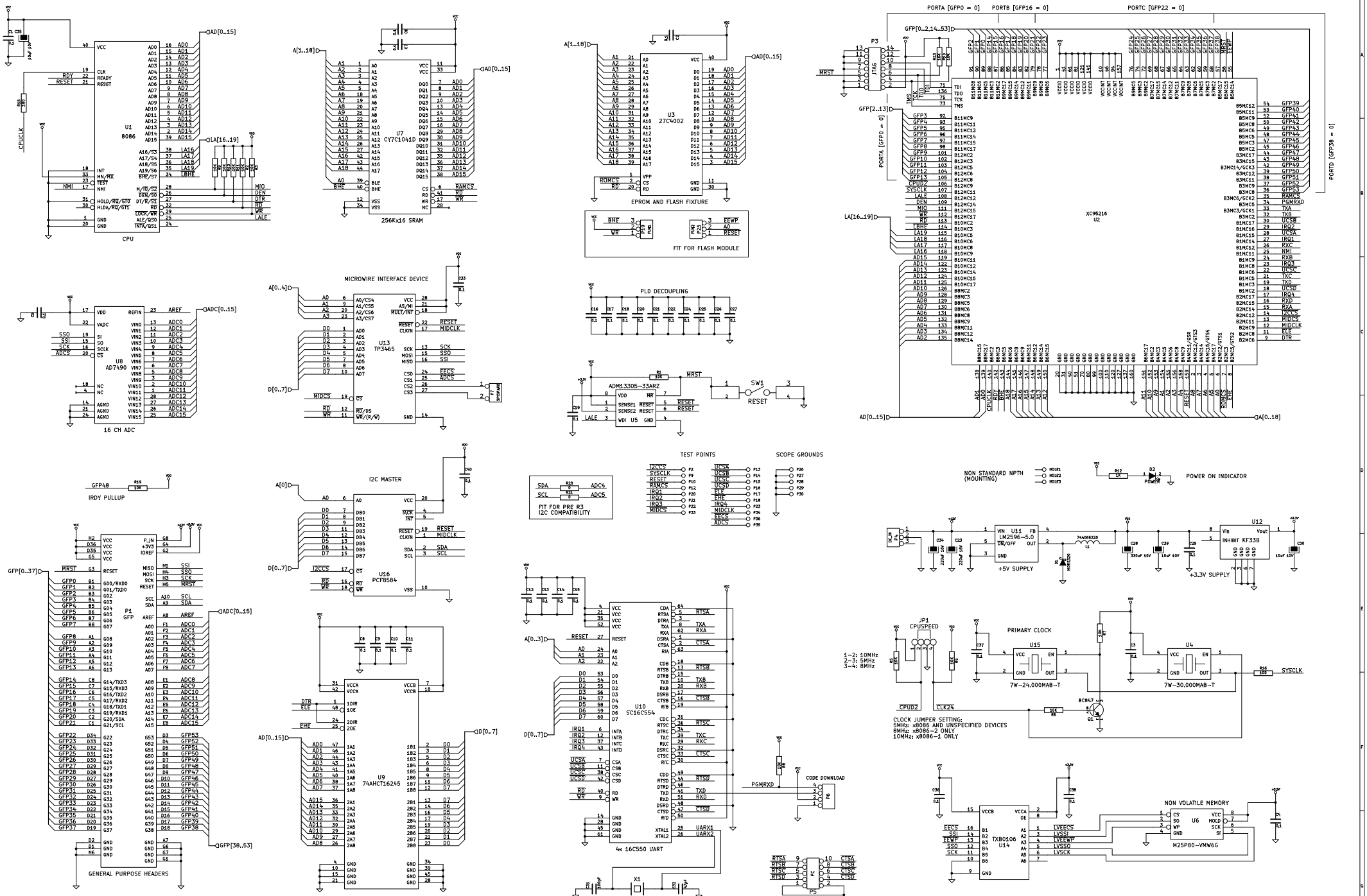
CPU
U1
8086
256Kx16 SRAM
U7
CY7C1041D
EPROM AND FLASH FIXTURE
U3
27C4002
FIT FOR FLASH MODULE
PORTA [GFP0 = 0]
PORTB [GFP16 = 0]
PORTC [GFP22 = 0]
PORTD [GFP38 = 0]
XC95216
U2
JTAG
P3
MICROWIRE INTERFACE DEVICE
U13
TP3465
16 CH ADC
U8
AD7490
PLD DECOUPLING
ADM13305-33ARZ
U5
SW1
RESET
IRDY PULLUP
I2C MASTER
U16
PCF8584
FIT FOR PRE R3
I2C COMPATIBILITY
TEST POINTS
SCOPE GROUNDS
NON STANDARD NPTH
(MOUNTING)
POWER ON INDICATOR
+5V SUPPLY
+3.3V SUPPLY
PRIMARY CLOCK
CPUSPEED
1-2: 10MHz
2-3: 5MHz
3-4: 8MHz
CLOCK JUMPER SETTING:
5MHz: x8086 AND UNSPECIFIED DEVICES
8MHz: x8086-2 ONLY
10MHz: x8086-1 ONLY
GENERAL PURPOSE HEADERS
P1
GFP
U9
74AHCT16245
U10
SC16C554
4x 16C550 UART
CODE DOWNLOAD
LFXTAL010689
7.3728MHz 30PF CUT
NON VOLATILE MEMORY
U6
M25P80-VMW6G
TXB0106
U14
Matthew Millman
File: eightodulno.sch
Title: 8OD.2 (BE)
Size: A2
Date: 9 mar 2016
Rev:
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable
Id: 1/1